

09-25-00

EXPRESS MAIL NO.: EK039197493US

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jc891 U.S. PTO
09/22/00

IN THE UNITED STATES PATENT AND TRADEMARK

Assistant Commissioner for Patents
Washington, D.C. 20231

Attorney's Docket No.: Octiv-01

jc841 U.S. PTO
09/669069
09/22/00

FILING TRANSMITTAL

Transmitted herewith for filing is the Patent Application of: Leif Hakan Claesson

For: "TECHNIQUE FOR IMPROVING AUDIO CLARITY AND INTELLIGIBILITY AT REDUCED BIT RATES OVER A PACKET SWITCHING NETWORK"

ENCLOSURES

- ☒ 19 page application including specification, claims and abstract;
- ☒ 4 sheets, Figs. 1, 2, 3 and 4 of informal drawings;
- ☒ a Declaration, Power of Attorney & Petition (unsigned);
- ☒ a postcard for return to us as proof of receipt of the above documents.

And

- ☒ an unexecuted Assignment of the invention with an assignment cover sheet;
- ☐ Verified Statement Claiming Small Entity Status (37 CFR 1.9(f) and 1.27(b));
- ☐ IDS (form PTO-1449) and copies of references;
- ☐ an associate power of attorney;
- ☐ a certified copy of the priority document (Under 35 USC 119) is enclosed
- ☐ Other

TYPE OF FILING

- ☒ This application claims the benefit of an earlier filed U.S. Provisional Patent Application Serial No. 60/174,118 filed December 31, 1999. (35 USC 120).
- ☐ This application claims the benefit of the priority date of an earlier filed _____ application (35 USC 119).
- ☐ This is an application filed pursuant to 37 CFR 1.53, permitting receipt of a filing date upon filing of specification, claims and drawings, if required, with applicant being given a period of one month from the date of notice to file the fee and oath or declaration.
- ☒ In the event any parts of this application are missing, please treat this as a filing under 37 CFR 1.53 as defined just above.

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Date: September 22, 2000

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BASIC FEE Design Patent			\$155	\$	\$310
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Independent Claims	4	minus 3 = 1	x39 =	\$39	x78 =
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Dated: September 22, 2000

Respectfully submitted,



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0022360-092200

Specification

TECHNIQUES FOR IMPROVING AUDIO CLARITY AND INTELLIGIBILITY AT REDUCED BIT RATES OVER A DIGITAL NETWORK

5

BACKGROUND OF THE INVENTION

Cross Reference to Related Applications

This application claims the benefit of U.S. Provisional Application 60/174,118, filed on
10 December 31, 1999, and entitled "Techniques For Improving Audio Clarity and Intelligibility at
Reduced Bit Rates Over a Digital Network".

Field of the Invention

The present invention relates to techniques for improving transmission of audio signals
15 over a digital network and particularly to improving audio clarity and intelligibility at reduced
bit rates over a digital network.

Description of the Prior Art

The growth of the Internet is doubling every 18 months, with over 57 million Domain
20 hosts as of July 1999. In the United States, 42% of the population has Internet access. The use
of audio transmitted over the Internet is growing even faster. According to iRadio (Feb. 1999),
13% of all Americans have listened to radio on the world wide web, which is up from 6% only
half a year before. However, the delivery of audio over the Internet is limited by low bit rate
connections. The present invention enhances the quality of audio (Music or Voice) for
25 transmission over a digital network, such as the Internet, before it is transmitted over the

network. This invention enhances audio delivered separately or as part of a video download or video stream.

Audio that is broadcast over the Internet in real-time is called streaming audio. Radio stations, concerts, speeches and lectures are all delivered over the web in streaming form.

5 Encoders such as those offered by Microsoft and Real Audio reside on servers that deliver the audio stream at multiple bit rates over various connections (modem, T1, DSL, ISDN etc.) to the listener's computer. Upon receipt, the streamed data is decoded by a "player" that understands the particular encoding format.

Fig. 1 shows the basic transport path of audio over the network. The Audio Server 10
10 sends digital audio files through a connection such as a T1 line 12 to a digital network 18 such as the Internet using a defined protocol such as Transport Control Protocol/Internet Protocol (TCP/IP). From the network 18 the listener can connect his client computer 15 to the network 18 using a point-to-point (POP) connection 14. As the audio files enter the client computer they can be listened through the speakers 16.

15 To improve audio clarity and intelligibility it is desirable to equalize the amplitude of sound and music over time intervals as well as across the entire frequency spectrum. In particular, when music or voice becomes louder and softer and most of the high volume sound is concentrated in a narrow frequency band the need to equalize the sound amplitude over different frequencies becomes greater.

20 At present, there are radio broadcasting systems such as Orban and other music production systems capable of equalizing voice and music in real-time and over a range of frequencies. However, such systems generally require a sophisticated operator and powerful hardware for implementation, which makes them both labor-intensive and expensive. Due to its enhanced quality, transmission of processed audio at lower bit rates can have more clarity and

presence than transmission of non-processed audio at higher bit rates. The result is an increase in bandwidth availability in a given network.

Therefore, the need arises for a method and apparatus for improving audio transmission across any digital network, such as the Internet, in real-time and by enhancing audio quality and intelligibility at reduced bit rates.

SUMMARY OF THE INVENTION

Briefly, a dynamics processor, in accordance with an embodiment of the present invention, includes a non-linear automatic gain control (AGC) responsive to an input audio signal comprised of a plurality of frequency components, each frequency component having associated therewith an amplitude, said non-linear AGC adaptive to develop a gain-modified audio signal. A multiband cross-over device is responsive to the gain-modified audio signal and is adaptive to generate 'n' number of signals, each of said 'n' signals having an amplitude and further having a unique frequency band associated therewith. The dynamics processor further includes 'n' number of processing blocks, each of which is responsive to a respective one of said 'n' signals for modifying the amplitude of the 'n' signals to develop modified 'n' signals; and a mixer device is responsive to said modified 'n' signals and adaptive to combine the same, wherein the amplitude of the plurality of frequencies associated with the audio signal is modified in real-time thereby enhancing the audibility of the audio signal.

The foregoing and other objects, features and advantages of the present invention will be apparent from the following detailed description of the preferred embodiments which make reference to several figures of the drawing.

IN THE DRAWINGS

Fig. 1 shows a prior art communication system for processing sound signals.

Fig. 2 shows a generalized dynamics processor used in processing audio signals according to an implementation of the present invention.

Fig. 3(a) shows various stages in the multi-band cross over, according to an
5 implementation of the present invention.

Fig. 3(b) shows a flowchart outlining the computations required to obtain the low pass and high pass outputs.

Fig. 4 shows a flowchart outlining various stages in an AGC loop.

Fig. 5 show a flowchart outlining various stages in a non-linear AGC loop.

Fig. 6 shows a communication system playing audio files over a network with dynamics
10 processing SW.

Fig. 7 shows an application of dynamics processing SW in decoding audio files.

Fig. 8 shows an application of dynamics processing SW at the receiving end of a
communication system wherein audio files are decoded.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Referring now to Fig. 2, a block diagram of a generalized dynamics processor 30 is
shown for processing audio signals according to one implementation of the present invention.

20 The general dynamics processor 30 is implemented entirely in SW and may be incorporated within the audio server 10 shown in Fig. 1 or within any standard PC, a cell phone, a personal digital assistant (PDA), a wireless application device, etc. By employing the generalized dynamics processor 30, the present invention improves audio transmission across any digital network such as the Internet or a packet switching network as described in detail hereinbelow.

The input block 32 in Fig. 2 receives audio signals from an audio source (not shown in Fig. 2) such as a microphone, a telephone or a music playback system. The input block 32 converts the audio signals into pulse code modulated (PCM) samples, which represent sampled digital data, i.e., data that is sampled at regular basis. Subsequently, at the frequency shaping block 34, the very low and very high frequency components of the PCM samples are eliminated which may otherwise degrade the audio quality of the samples. Examples of the low frequency components are rumble and hum and examples of high frequency components are noise and hiss.

At the 2-band crossover block 36 the audio samples are separated into two partially overlapping frequency bands. Each frequency band is subsequently processed at non-linear automatic gain control (AGC) loop blocks 38 and 40. In the non-linear AGC loops 38 and 40 each of the input samples is multiplied by a number known as the gain factor. Depending on whether the gain factor is greater or lower than 1.0, the volume of the input sample is either increased or decreased for the purpose of equalizing the amplitude of the input samples in each of the frequency bands. The gain factor is variable for different input samples as described in more detail hereinbelow. The distinguishing factor between a non-linear AGC and an AGC is that the gain factor varies according to a nonlinear mathematical function in the non-linear AGC. Thus, the output of each of the non-linear AGCs 38 and 40 is the product of the input sample and the gain factor. The output of the two non-linear AGCs is mixed at the mixer block 42 so that in the resulting output all the frequencies are represented.

At the next block, multi-band crossover 44, the PCM samples are broken down into various overlapping frequency bands, which may number 3,4,5,6,7 or more. In this way, the multi-band crossover 44 behaves very similar to the 2-band crossover 36 except that the former has more frequency bands. The main reason for breaking down the samples into various frequencies is that the volume in each frequency band may be equalized separately and

independently from the other frequency bands. Independent processing of each frequency band is necessary in most cases such as in music broadcasting where there is a combination of high-pitch, low-pitch and medium-pitch instruments playing simultaneously. In the presence of a high-pitch sound, such as crash of a symbol that is louder than any other instrument for a fraction of a second, a single band AGC would reduce the amplitude of the entire sample including the low and medium frequency components present in the sample that may have originated from a vocalist or a bass. The result is a degradation of audio quality and introduction of undesirable artifacts into the music. A one band AGC would allow the component of frequency with the highest volume to control the entire sample, a phenomenon referred to as spectral gain intermodulation.

According to one implementation of the present invention as shown in Fig. 2, the multi-band crossover 44 allows independent processing of various frequency bands. Consequently the volume of the high-pitch component of the sample may be reduced without affecting the other frequency components, avoiding spectral gain intermodulation.

As shown in Fig. 2 the sample is decomposed into n separate frequency bands. Each band is subsequently treated independently as indicated by processing blocks 60, 62, and 64. Processing block 60 is dedicated to processing band 1 with components possessing the lowest frequency. Block 46, labeled drive 1, represents a type of gain control wherein the gain factor is an adjustable parameter that is preset by the user. For instance the user may decide that in a particular case the quality of music improves when high frequency components are controlled more than the middle and lower frequency components. Then the user presets the drive factors in drive 1 block 46 and all the other drive 1 blocks in the remaining frequency bands to accommodate such an outcome.

The next step in dynamics processing is the processing block AGC 48 wherein the lowest frequency components of the sample are multiplied by a gain factor in order to either

increase or decrease the volume accordingly as explained in more detail hereinbelow. The drive 2 block 50 acts in exactly the same manner as drive 1 block 46 except with a different gain factor that is preset by the user. The gain factor set by the user in the drive 2 blocks in all the frequency bands may be different in order to effect a particular outcome.

- 5 The next step is the negative attack time limiter 52. In step 52 volume of the frequency band is adjusted based on signals in the future. To elaborate, samples are stored in a delay buffer so that the future samples may be used in equalizing the volume. When the buffer is full, a small block of earlier samples is appended to the beginning of the buffer and a block of samples is saved from the end of the buffer. The future sample is multiplied by the gain factor.
- 10 If the resulting data has an amplitude greater than a threshold value (a user-fixed parameter) the gain factor is reduced to a value equal to the threshold value divided by the amplitude of the future sample. A counter referred to as the release counter is subsequently set equal to the length of the delay buffer. The resulting data is then passed through a low-pass filter so as to smooth out any abrupt changes in the gain that will have resulted from multiplication by the
- 15 future sample.

Finally, the sample in the buffer which has been delayed is multiplied by the gain factor computed above in order to produce the output. Subsequently, the release counter is decremented. If the release counter is less than zero, the gain factor is multiplied by a number slightly greater than 1.0. Finally, the next sample is read and the above process is repeated.

- 20 Accordingly, calculation of the gain factor in the negative attack time limiter 52 is based on the future sample. The main function of the negative attack time limiter 52 is to ensure that the transition from the present sample to the future sample is achieved in a smooth and inaudible fashion, and to remove peaks on the audio signal that waste bandwidth.

- 25 At the next step 54, the inverse drive 2, the sample is multiplied by a gain factor, which is the reciprocal of the gain factor used in the drive 2 block 50. At the soft clip block 56 the

amplitude of the sample is truncated at a certain level of amplitude. However, a smooth signal that is truncated at a certain level of amplitude develops sharp edges. Sharp edges when passed through subsequent stages of processing can result in overshoots that are narrow regions of large amplitude at the two edges of the truncated sample resulting in audio distortion. Soft clipping
 5 alleviates the consequences of audible distortion by reducing the amplitude by which the sample overshoots at the edges. However, the overshoots at the edges are not completely eliminated. The soft clip step 56 is peculiar to the lowest frequency band which helps to create a “punchy” bass sound. The remaining $n-1$ bands lack such a step. The remaining blocks in all the frequency bands are identical.

10 The level mixer block 58 acts as another gain control wherein the sample is multiplied by a gain factor that is a user-programmable feature of this invention and is preset by the user. The level mixer 58 represents the last stage before outputs of different frequency bands are mixed. Mixing of the outputs of the different frequency bands is performed at the mix block 66. Step 68, the drive, is a gain control that is preset by the user. The drive control at step 68 is applied to
 15 the entire sample composed of all the frequencies. Similarly, the negative attack time limiter 70 acts exactly in the same manner as block 52 except that at step 70 the sample with all the frequencies is being processed. Finally, at step 72, the output of the generalized dynamics processor in the form of PCM samples is transmitted to a destination point not shown in Fig. 2.

Figures 3(a) and 3(b) show various stages 80 of processing in the multi-band crossover
 20 44 of Fig. 2. At each stage of the multi-band crossover 44, as shown in Fig. 3(b), a computation is performed resulting in a high pass output as shown in the loop 90. More specifically, at each stage corresponding to a particular frequency band the next sample as well as the output from the previous stage, referred to as the high pass output, are read. An averaging process is then performed wherein the weighted sum of the previous stage’s output and the new sample is
 25 computed. The output of the averaging process is labeled the low-pass output in Figures 3(a)

and 3(b). Thus, there are $n-1$ low pass outputs corresponding to the n frequency bands. The difference between the input sample and the low pass output is denoted as the high pass output, which forms the input to the next stage of the multi-band crossover. Fig. 3(a) shows four stages corresponding to the 1st, 2nd, 3rd, and 4th stages of the multi-band crossover labeled 82-88, respectively. At each stage, except the 1st stage 82, the inputs are the input sample and the high pass outputs as calculated according to block 90 and explained hereinabove.

Fig. 4 shows a flowchart outlining various stages in an AGC loop 98. The operation of AGC 48 of Fig. 2 was described briefly hereinabove and is now explained in more detail. AGC loop 98 is performed for each new sample that is read by the AGC. Initially a gain factor is assumed and thereafter for each 64th sample, as indicated at step 92, the gain factor is increased slightly through multiplication by a number greater than 1.0, referred to as the release rate parameter. In this way, the gain factor increases with every 64th sample. Every input sample is multiplied by the gain factor thus obtained, as indicated at step 94. At step 96 it is determined if as the result of multiplication the amplitude of the sample exceeds a preset threshold value. In the event the threshold value is exceeded, the gain factor is reduced slightly through multiplication by a number slightly less than 1.0 known as the attack rate parameter. Otherwise the gain factor remains unaltered and the process repeats by reading a new input sample.

Fig. 5 shows a flowchart outlining various stages in a special AGC loop 100. A brief description of the operation of the non-linear AGC loop 38 in Fig. 2 was presented hereinabove.

In Fig. 5, additional details regarding the non-linear AGC loop 100 is provided. The non-linear AGC loop 100 is performed for each new input sample. At step 102, the gain factor is increased for every 64th sample read by multiplying the gain factor with a number slightly greater than 1.0, i.e. the release rate parameter. At step 104, initially a trial multiplication is performed by multiplying each input sample with the gain factor. If the amplitude of the resulting signal is greater than a preset threshold value, the gain factor is reduced slightly by being multiplied by a

number slightly less than 1.0, i.e. the attack rate parameter. The gain factor is then modified according to a nonlinear function.

In one implementation of the present invention, the new gain factor is obtained by dividing the old gain factor by two and adding a fixed value to the outcome, thereby obtaining a nonlinear variation in the gain factor. The final output of the non-linear AGC loop 100 is obtained by multiplying each input sample by the modified gain factor. Thereafter, the process is repeated for the incoming new input samples.

The present invention is implemented entirely in software. In one implementation of the present invention a pentium processor within a standard PC is programmed in assembly language to perform the generalized dynamics processing depicted in Fig. 2, resulting in considerable reduction in both expense and complexity. Furthermore, the present invention is implemented in real-time making it particularly desirable in the transmission of audio signals over any digital network such as the Internet.

Fig. 6 depicts one application of the present invention wherein audio files are played over a digital network with dynamic processing optimization. In Fig. 6 is shown a communication system 120 comprising an audio server 106, a digital network 110, a PC 114 and speakers 118. Audio server 106 is coupled to the digital network 110 through the transmission line 108, which may be a T1 line, the digital network 110 is coupled to the PC 114 through the transmission line 112 and the PC 114 is coupled to the speakers 118 through the line 116.

Within the audio server 106, which may be a PC or several connected PC's, are shown several subunits, that are dedicated to the processing of audio signals. The audio files 122 stored on a disk may be encoded in some type of encoding algorithm such as MP3 within the audio server 106. The audio files are played at step 124 using a decoding SW such as Winamp and are subsequently converted to PCM samples. The PCM samples are then processed by the generalized dynamics processing SW 126, an embodiment thereof is shown in Fig. 2. The

output of the dynamics processing SW 126 is encoded again using some type of encoding algorithm such as MP3 and is transmitted through the line 108, across the digital network 110, and through the line 112 to the PC 114. Inside the PC 114, equipped with the appropriate decoding SW such as Winamp, the samples are decoded and converted into audio signals which are then fed to the speakers 118 through the line 116.

Fig. 7 shows another application of the present invention wherein a user is playing audio files stored in a PC 130 with dynamics processing optimization. Shown in Fig. 7 are a PC 130 and speaker 134 coupled through the line 132. The PC 130 may be located inside the user's car and the user may want to use dynamic processing SW in order to improve the quality of sound in the presence of background noise inside the car.

The audio files 136 are encoded using some encoding algorithm such as MP3 inside the PC. The audio files are decoded at step 138 by a decoding SW and are converted to PCM samples. The PCM samples are processed by the dynamics processing SW 140. The dynamics processing SW 140 employed in the PC 130 or in a phone or in a PAD may employ fewer frequency bands and as a result would be less powerful than that described in Fig. 6. The main reason for employing less powerful dynamics processing SW is that the more frequency bands are present within the SW the more computationally intensive the task of dynamic processing becomes; this might be too great a burden on a processor such as the one inside the PC 130. Such limitations do not exist for audio servers such as 106 in Fig. 6 and accordingly more powerful dynamics processing SW are employed therein. The output of the dynamics processing SW in the form of PCM samples is converted to audio signals at the sound card driver 142 which are fed through the line 132 to the speakers 134 to be played.

Fig. 8 shows another application of the present invention wherein the dynamics processing SW is employed at the receiving end of a network communication system. Shown in Fig. 8 is a communication system 170 including an audio server 150, a digital network 154, a PC

158 and speakers 162. The audio server 150 is coupled to the digital network 154 through the transmission line 152 and the digital network 154 is coupled to the PC 158 through the transmission line 156 and the PC 158 is linked to the speakers 162 through the line 160.

The audio server 150 in this case does not include dynamics processing SW. The
5 encoded PCM samples are transmitted from the audio server 150 through the transmission line 152, across the digital network 154 and through the transmission line 156 to the PC 158. Inside the PC 158, the PCM samples are decoded at step 164 using an appropriate decoding SW. At step 166 the PCM samples are processed by the dynamic processing SW. The output of the dynamics processing SW is converted into audio signals by the sound card driver at step 168 and
10 is subsequently fed to the speakers 162 through the line 160 to be played.

As discussed hereinabove, the present invention improves audio transmission across any digital network such as the Internet by enhancing audio quality and intelligibility at reduced bit rates. One of the main advantages of the present invention, as discussed in full detail hereinbelow, is that the processing of the audio signals is performed in real-time without the
15 need for an operator. In addition, the present invention is implemented entirely in software (SW), such as on a standard personal computer (PC), resulting in a system much less expensive and less complex than the sound processing systems presently available.

Although the present invention has been described in terms of specific embodiments it is anticipated that alterations and modifications thereof will no doubt become apparent to those
20 skilled in the art. It is therefore intended that the following claims be interpreted as covering all such alterations and modification as fall within the true spirit and scope of the invention.

What is claimed is:

CLAIMS

1. A dynamics processor comprising:

a non-linear automatic gain control (AGC) responsive to an input audio signal comprised of a plurality of frequency components, each frequency component having associated therewith an amplitude, said non-linear AGC adaptive to develop a modified gain audio signal;

a multiband cross-over device responsive to the modified gain audio signal and adaptive to generate 'n' number of signals, each of said 'n' signals having an amplitude and further having a unique frequency band associated therewith;

'n' number of processing blocks, each of which responsive to a respective one of said 'n' signals for modifying the amplitude of the 'n' signals to develop modified 'n' signals; and

a mixer device responsive to said modified 'n' signals and adaptive to combine the same, wherein the amplitude of the plurality of frequencies associated with the audio signal is modified in real-time thereby enhancing the audibility of the audio signal.

2. A dynamics processor as recited in claim 1 wherein said mixer device is adaptive to provide an output for transmission thereof over the Internet.

3. A dynamics processor as recited in claim 1 wherein said non-linear AGC for multiplying said input audio signal by gain factors varying in a non-linear manner.

4. A dynamics processor as recited in claim 1 comprising a cross-over block responsive to said input audio signal and adaptive to divide the input audio signal into cross-over signals having two or more frequency bands, said dynamics processor including additional non-linear

4 AGCs, each of which responsive to a respective cross-over signal, said non-linear AGCs and
5 non-linear AGC developing at least two pre-input mixer signals.

1 5. A dynamics processor as recited in claim 4 comprising an input mixer device responsive
2 to said at least two pre-input mixer signals for combining the same to develop said modified
3 gain audio signal.

1 6. A dynamics processor as recited in claim 1 wherein each of said 'n' number of
2 processing blocks includes a processing block AGC (48 in Fig. 2) coupled to a negative attack
3 time limiter, and a level mixer coupled to the negative attack time limiter, the processing block
4 AGC responsive to said respective one of said 'n' signals.

1 7. A dynamics processor as recited in claim 6 wherein each of said 'n' number of
2 processing blocks further includes a first drive circuit responsive to said respective one of said
3 'n' signals and coupled to said processing block AGC, a second drive circuit coupled between
4 said processing AGC and said negative attack time limiter and an inverse drive circuit coupled
5 between said negative attack time limiter and said level mixer, said first and second drive circuit
6 for adjusting the amplitude of said respective one of said 'n' signals by a gain factor determined
7 by a user.

1 8. A dynamics processor as recited in claim 7 wherein said level mixer for programmably
2 adjusting the amplitude of said respective one of said 'n' signals by a gain factor.

1 9. A dynamics processor as recited in claim 7 wherein said inverse drive circuit for
2 adjusting said respective one of said 'n' signals by one divided by the gain factor.

1 10. A dynamics processor as recited in claim 6 wherein one of said 'n' number of processing
2 blocks includes a soft clip device coupled between said negative attack time limiter and said
3 level mixer and responsive to said respective one of said 'n' signals, said soft clip device for
4 truncating the amplitude of said respective one of said 'n' signals when the amplitude is above a
5 predetermined level thereby developing a signal having overshoots, said overshoots having
6 amplitudes, the soft clip device further for decreasing the amplitude of the overshoots thereby
7 enhancing the audibility of the signal.

1 11. A method of dynamically processing an audio signal comprising:
2 receiving an input audio signal comprised of a plurality of frequency components, each
3 frequency component having associated therewith an amplitude;
4 modifying the input audio signal;
5 generating 'n' number of signals from said modified input audio signal, each of said 'n'
6 signals having an amplitude and further having a unique frequency band associated
7 therewith;
8 modifying the amplitude of the 'n' signals; and
9 combining said modified 'n' signals,
10 wherein the amplitude of the plurality of frequencies associated with the audio signal is
11 modified in real-time thereby enhancing the audibility of the audio signal.

1 12. A method of dynamically processing an audio signal as recited in claim 10 wherein said
2 steps in claim 10 are performed in assembly code thereby improving the efficiency of
3 said processing.

13. A computer readable medium having stored therein computer readable program code comprising instructions for performing the following steps:

receiving an input audio signal comprised of a plurality of frequency components, each frequency component having associated therewith an amplitude;

modifying the input audio signal;

generating 'n' number of signals from said modified input audio signal, each of said 'n' signals having an amplitude and further having a unique frequency band associated therewith;

modifying the amplitude of the 'n' signals; and

combining said modified 'n' signals,

wherein the amplitude of the plurality of frequencies associated with the audio signal is modified in real-time thereby enhancing the audibility of the audio signal.

14. A dynamics processor comprising:

non-linear automatic gain control (AGC) means responsive to an input audio signal comprised of a plurality of frequency components, each frequency component having associated therewith an amplitude, said non-linear AGC adaptive to develop a modified gain audio signal;

multiband cross-over means responsive to the modified gain audio signal and adaptive to generate 'n' number of signals, each of said 'n' signals having an amplitude and further having a unique frequency band associated therewith;

'n' number of processing blocks, each of which responsive to a respective one of said 'n' signals for modifying the amplitude of the 'n' signals; and

mixer means responsive to said modified 'n' signals and adaptive to combine the same,

12 wherein the amplitude of the plurality of frequencies associated with the audio signal is
13 modified in real-time thereby enhancing the audibility of the audio signal.

1 15. A dynamics processor as recited in claim 14 wherein said non-linear AGC means for
2 multiplying said input audio signal by gain factors varying in a non-linear manner.

1 16. A dynamics processor as recited in claim 14 comprising a cross-over block responsive to
2 said input audio signal and adaptive to divide the input audio signal into cross-over signals
3 having two or more frequency bands, said dynamics processor including additional non-linear
4 AGC means, each of which responsive to a respective cross-over signal, said non-linear AGC
5 means and non-linear AGC means for developing at least two pre-input mixer signals.

1 17. A dynamics processor as recited in claim 16 comprising an input mixer means
2 responsive to said at least two pre-input mixer signals for combining the same to develop said
3 modified gain audio signal.

1 18. A dynamics processor as recited in claim 14 wherein each of said 'n' number of
2 processing means includes a processing block AGC coupled to a negative attack time limiter,
3 said each of said 'n' number of processing blocks further including a level mixer coupled to the
4 negative attack time limiter, the processing block AGC responsive to said respective one of said
5 'n' signals.

1 19. A dynamics processor as recited in claim 18 wherein each of said 'n' number of
2 processing blocks further includes a first driver responsive to said respective one of said 'n'
3 signals and coupled to said processing block AGC, a second driver coupled between said

- 4 processing AGC and said negative attack time limiter and an inverse driver coupled between
- 5 said negative attach time limiter and said level mixer.

ABSTRACT

In accordance with an embodiment of the present invention, a dynamics processor includes a non-linear automatic gain control (AGC) responsive to an input audio signal comprised of a plurality of frequency components, each frequency component having associated therewith an amplitude, said non-linear AGC adaptive to develop a modified gain audio signal. A multiband cross-over device is responsive to the modified gain audio signal and is adaptive to generate 'n' number of signals, each of said 'n' signals having an amplitude and further having a unique frequency band associated therewith. The dynamics processor further includes 'n' number of processing blocks, each of which is responsive to a respective one of said 'n' signals for modifying the amplitude of the 'n' signals to develop modified 'n' signals; and a mixer device is responsive to said modified 'n' signals and adaptive to combine the same, wherein the amplitude of the plurality of frequencies associated with the audio signal is modified in real-time thereby enhancing the audibility of the audio signal.

The diagram illustrates a networked audio system architecture. On the left, an 'Audio Server' (represented by a computer monitor and tower) is connected to a central 'Network' (represented by a circle). A thick arrow labeled '2' points from the Audio Server to the Network. A curved line labeled '1' connects the Audio Server to a 'Client Computer' on the right. The Client Computer is also connected to the Network by a thick arrow labeled '4'. The Client Computer is further connected to two 'Speakers' (represented by speaker icons) by a curved line labeled '5'. A curved line labeled '3' connects the Network to the Client Computer. The label 'Speakers 6' is positioned above the two speakers.

Fig. 1

Fig. 2

Block diagram of a system for processing audio signals, labeled Fig. 2. The system includes an ADC (12), Dynamic Enhancer (22), Encoder (24), Audio Stream Network Server (26), Network (28), Decoder (30), DAC (32), and two speakers (10). The signal flow is from input 1 through the ADC, Dynamic Enhancer, Encoder, Audio Stream Network Server, Network, Decoder, DAC, and finally to the speakers. Handwritten numbers 1 through 11 are present above the components.

Fig. 3

DECLARATION, POWER OF ATTORNEY AND PETITION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole of the subject matter which is claimed and for which a patent is sought on the invention entitled "TECHNIQUE FOR IMPROVING AUDIO CLARITY AND INTELLIGIBILITY AT REDUCED BIT RATES OVER A PACKET SWITCHING NETWORK" the specification of which

☒ is attached hereto

☐ was filed on _____ as Application Serial No. _____
and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) or U.S. provisional application(s) for patent or inventor's certificate listed below and have also identified below any foreign application or U.S. provisional application(s) for patent or inventor's certificate having a filing date before that of the application of which priority is claimed.

Prior Foreign/U.S. Provisional Application(s)

(Number)	(Country)	(Day, month, year filed)	Priority Claimed	
60/174,118	US	31 December 1999	<input checked="" type="checkbox"/> Yes	No
(Number)	(Country)	(Day, month, year filed)	Yes	No
(Number)	(Country)	(Day, month, year filed)	Yes	No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	Filing Date	(Status: Patented, pending, abandoned)
(Application Serial No.)	Filing Date	(Status: Patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

And I hereby appoint MARYAM IMAM Reg. 38,190 of IMAM & ASSOCIATES, Two North Second St., Suite 1100, San Jose, CA 95113 (408) 271-8752, as my attorneys with full power of substitution and revocation, to prosecute said application and to transact in connection therewith all business in the Patent and Trademark Office and before competent International Authorities.

Address all telephone calls to Maryam Imam at (408) 271-8752 and address all correspondence to:

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Wherefore I pray that Letters Patent be granted to me for the invention or discovery described and claimed in the foregoing specification and claims, and I hereby subscribe my name to the foregoing specification and claims, declaration, power of attorney, and this petition.

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096609-2200